

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY-GURUJADA VIZINAGARAM
III B. Tech I Semester Regular Examinations November -2025
INFORMATION THEORY AND ERROR CONTROL CODING
(ECE)

Time: 3 hours

Max. Marks: 70

The Question paper consists of Part A & Part B.

Part A is compulsory, Answer all questions. Part B Answers any one question from each unit.

1		PART-A	(20Marks)
	a)	Write any two differences between <i>Shannon-Fano</i> and <i>Huffman</i> coding.	[2]
	b)	Define <i>Binary Symmetric Channel (BSC)</i> and its crossover probability.	[2]
	c)	What are <i>systematic</i> and <i>non-systematic</i> codes?	[2]
	d)	Write the condition satisfied by <i>Generator matrix (G)</i> and <i>Parity-check matrix (H)</i> .	[2]
	e)	Differentiate between <i>block</i> and <i>convolutional</i> codes.	[2]
	f)	What is <i>Concatenated coding</i> ?	[2]
	g)	Define <i>Iterative decoding</i> .	[2]
	h)	What is the purpose of <i>Soft-Input Soft-Output (SISO)</i> decoding?	[2]
	i)	What is <i>soft decision decoding</i> ?	[2]
	j)	What is the purpose of <i>edge connections</i> in Tanner graphs?	[2]
		PART-B	(50Marks)
		Unit - I	
2	a)	State and prove <i>Kraft's Inequality</i> for any instantaneous code. How does it determine the existence of a prefix-free code for a given set of codeword lengths?	[5]
	b)	For a Binary Symmetric Channel (BSC) with error probability $p = 0.1$, derive the channel capacity. Compare it with that of a Binary Erasure Channel (BEC) with erasure probability $p = 0.1$.	[5]
		(OR)	
3	a)	Construct the <i>Huffman code</i> for a source with symbols {A, B, C, D, E} having probabilities {0.3, 0.25, 0.2, 0.15, 0.1}. Compute the average code length, entropy, and coding efficiency.	[5]
	b)	State and explain <i>Shannon's Source Coding Theorem</i> . Derive the limits on the average code length for any lossless source coding scheme.	[5]
		Unit - II	
4	a)	Explain the <i>Standard Array Decoding</i> method for linear block codes. Illustrate with an example for a (6,3) code	[5]
	b)	Define a <i>Linear Block Code (n, k)</i> . Derive the relation between <i>Generator Matrix (G)</i> and <i>Parity Check Matrix (H)</i> and show that $GH^T = 0$.	[5]
		(OR)	
5	a)	For the (7,4) <i>Hamming Code</i> , construct the <i>Generator Matrix</i> and <i>Parity Check Matrix</i> . Encode the message 1011 and find the syndrome if an error occurs in the 5th bit.	[5]
	b)	Describe the encoding process of <i>Cyclic Codes</i> using	[5]

		<i>Generator Polynomial.</i> For $g(x) = 1 + x + x^3$, encode the message 1011.	
		Unit - III	
6	a)	Draw the <i>State Diagram</i> and <i>Trellis Diagram</i> for a rate $\frac{1}{2}$, constraint length 3 convolutional encoder with generator sequences $G_1 = [1,1,1]$ and $G_2 = [1,0,1]$. Encode the sequence 1101.	[5]
	b)	Explain the <i>Viterbi Algorithm</i> for decoding convolutional codes. Show step-by-step decoding for a simple 3-bit input sequence using path metric calculation.	[5]
		(OR)	
7	a)	Discuss the types of <i>ARQ protocols</i> (Stop-and-Wait, Go-Back-N, Selective Repeat). Derive an expression for throughput efficiency for Stop-and-Wait ARQ.	[5]
	b)	Explain the concept of <i>Concatenated Coding</i> . How are convolutional and block codes combined in practice (e.g., in space communication systems)?	[5]
		Unit - IV	
8	a)	Explain the <i>structure</i> and <i>encoding</i> process of a <i>Parallel Concatenated Turbo Code</i> . Illustrate with a block diagram showing two RSC encoders and interleaver.	[5]
	b)	Describe the <i>Iterative Decoding Process</i> of Turbo Codes using the <i>Soft-Input Soft-Output (SISO)</i> approach. Discuss how extrinsic information improves performance.	[5]
		(OR)	
9	a)	Differentiate between <i>Serial</i> and <i>Parallel Concatenated Turbo Codes</i> . Explain their respective advantages.	[5]
	b)	Explain the role of <i>Interleaver Design</i> in Turbo Codes. What parameters affect the performance of the Turbo Decoder?	[5]
		Unit - V	
10	a)	What are <i>Low-Density Parity-Check (LDPC) Codes</i> ? Describe their <i>Tanner Graph</i> representation and explain how sparsity affects decoding complexity.	[5]
	b)	Explain the <i>Belief Propagation (Sum-Product)</i> decoding algorithm for LDPC codes. Illustrate one iteration of message passing on a simple 4-bit parity-check graph.	[5]
		(OR)	
11	a)	Discuss the <i>construction and encoding</i> of LDPC codes using a <i>Parity Check Matrix</i> with column and row weights (3,6).	[5]
	b)	Write short notes on <i>Spatially Coupled LDPC Codes</i> . How do they improve decoding threshold compared to regular LDPC codes?	[5]
